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V. Vanna

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshiya SATOH et al.
Serial No.: Not Yet Assigned
Filed: March 16, 2001
For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD
THEREOF
Group: Not Yet Assigned
Examiner: Not Yet Assigned

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner
For Patents
Washington, D.C. 20231

June 18, 2001

Sir:

In the matter of the above-identified application, applicants are submitting copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This Information Disclosure Statement is being submitted before the mailing date of a first office action on the merits. Therefore, no fee is incurred.

The documents listed on the attached form equivalent to Form PTO-1449 are not in the English language. However, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance will be satisfied when an English language translation citing the documents is obtained from the translator and will be duly submitted to the USPTO in due course.

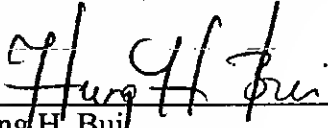
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In the interim, Applicants respectfully request that this information disclosure statement be considered and entered as a matter of records by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 503.39864X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP


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Registration No. 40,415

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Attachments

HHB/srm